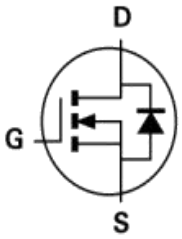
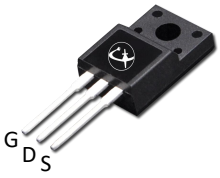
LC65R900F

N-channel Super Junction MOSFET

MAIN CHARACTERISTICS

I_D	7A
V_{DSS}	650V
RDSON-typ VGS=10V	610mΩ





TO-220F

FEATURES

- Low gate charge
- Very low switching and conduction loss
- Extremely high commutation ruggedness

APPLICATIONS

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS）
- Power Factor Correction(PFC)

MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275℃ maximum,10s per JESD 22-B106

Product specification classification

Part Number	Package	Mode Name	Pack
LC65R900F	TO-220F	LC65R900F	Tube

LC65R900F

N-channel Super Junction MOSFET

Maximum Ratings at Tc=25°C unless otherwise specified

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	±30	V
Continue Drain Current	I_D	7	A
Pulsed Drain Current (Note1)	I_{DM}	24	A
Power Dissipation	P_D	28	W
Single Pulse Avalanche Energy (Note1)	E_{AS}	129	mJ
Operating Temperature Range	T_J	-50 to +150	°C
Storage Temperature Range	T_{STG}	-50 to +150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	4.8	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62	°C/W

Note1:Pulse test: 300 μs pulse width, 2 % duty cycle

Electrical Characteristics at Tc=25°C unless otherwise specified

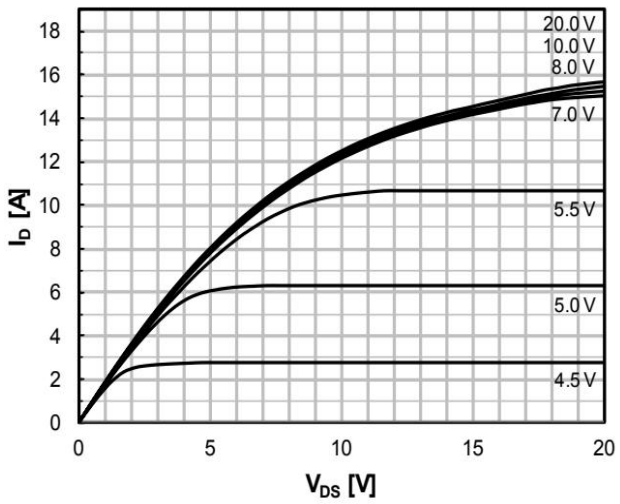
Parameter	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{GS} = 0 V,I_D = 250 μA	BV_{DSS}	650	-	-	V
Drain-Source Leakage Current	V_{DS} =650 V, V_{GS} = 0 V	I_{DSS}	-	-	1	μA
Gate Leakage Current	V_{GS} = ± 30 V, V_{DS} = 0 V	I_{GSS}	-	-	±100	nA
Gate-Source Threshold Voltage	V_{DS} = V_{GS} , I_D = 250 μA	$V_{GS(th)}$	2	-	4	V
Drain-Source On-State Resistance	VGS=10V, ID=3.5A	RDS(on)	-	610	830	mΩ
Forward Transconductance	V_{DS} = 5 V, I_D = 2 A	gfs	-	-	8	S
Input Capacitance	VDS=50V , VGS=0V , f=1MHz	C_{iss}	-	715	-	pF
Output Capacitance		C_{oss}	-	35	-	pF
Reverse Transfer Capacitance		C_{rss}	-	1.7	-	pF
Turn-on Delay Time(Note2)	VDD = 325V, ID = 7A, VGS =10V, RG =25Ω	$t_{d(ON)}$	-	17	-	ns
Rise Time(Note2)		t_r	-	26	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	53	-	ns
Fall Time(Note2)		t_f	-	38	-	ns
Total Gate Charge(Note2)	VDS = 520V, VGS = 10V, ID = 7A	Q_G	-	13	-	nC
Gate to Source Charge(Note2)		Q_{GS}	-	2.1	-	nC
Gate to Drain Charge(Note2)		Q_{GD}	-	6.9	-	nC

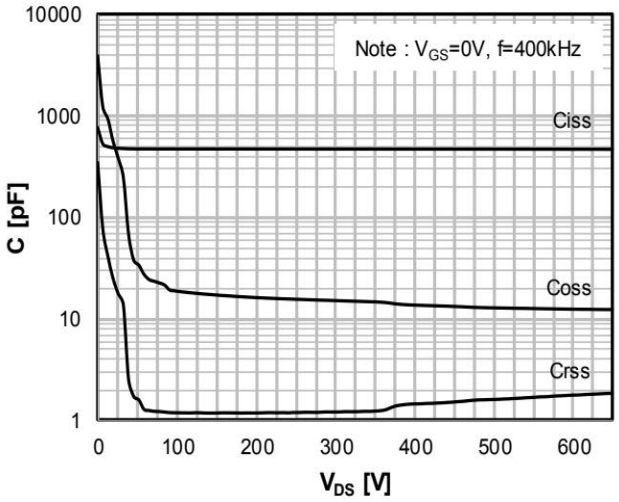
Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified

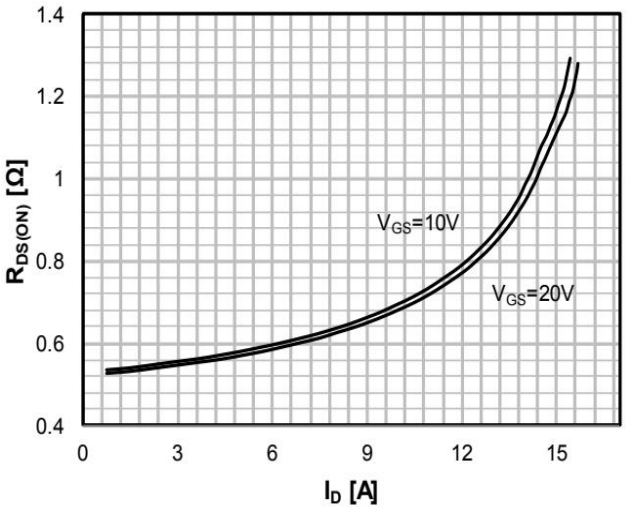
Parameter	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current		I_S	-	-	7	A
Maximun Body-Diode Pulsed Current(Note2)		I_{SM}	-	-	24	A
Drain-Source Diode Forward Voltage	VGS=0V , IS=3.5A , TJ=25℃	V_{SD}	-	-	1.4	V
Body Diode Reverse Recovery Time	VR=30V,IF=1A, dI/dt=100A/μs	Trr	-	151	-	ns
Body Diode Reverse Recovery Charge		Qrr	-	565	-	nC

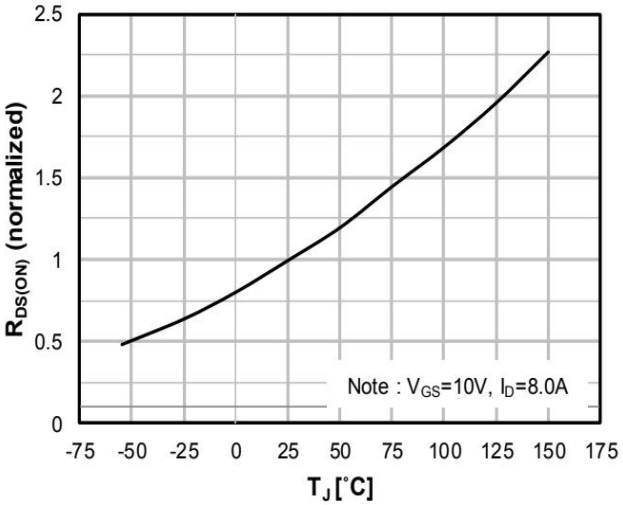
Note2:Pulse test: 300 μs pulse width, 2 % duty cycle

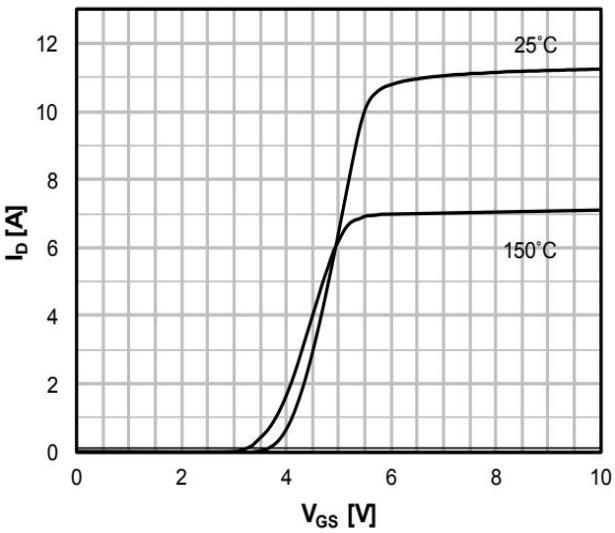
RATINGS AND CHARACTERISTIC CURVES

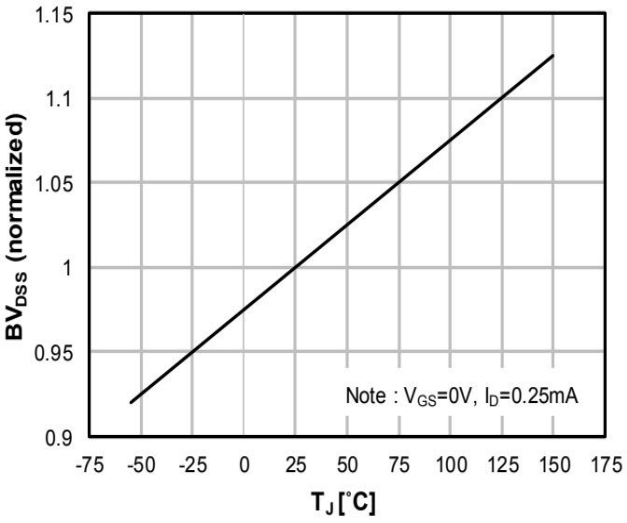




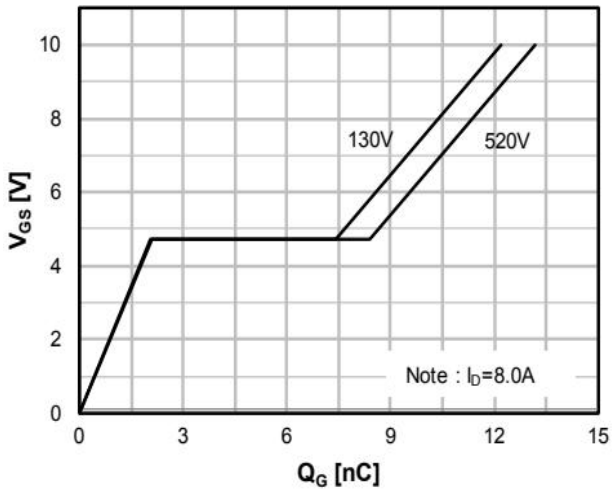


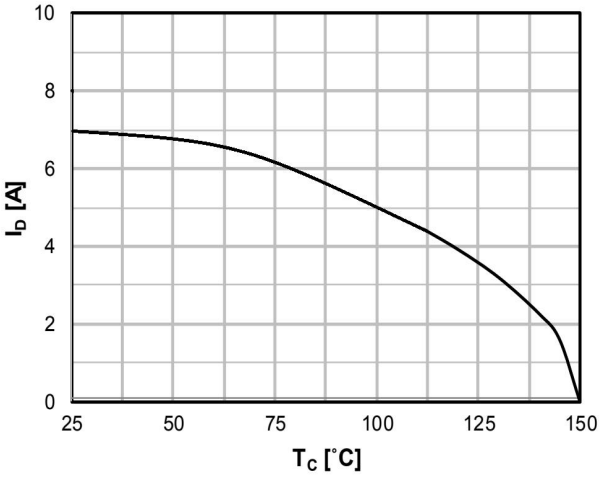


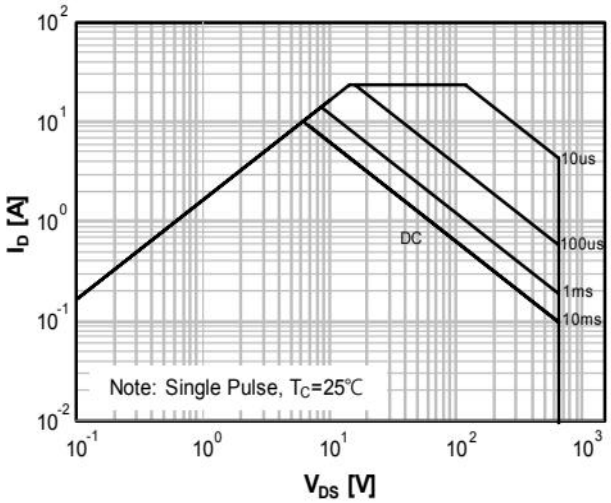


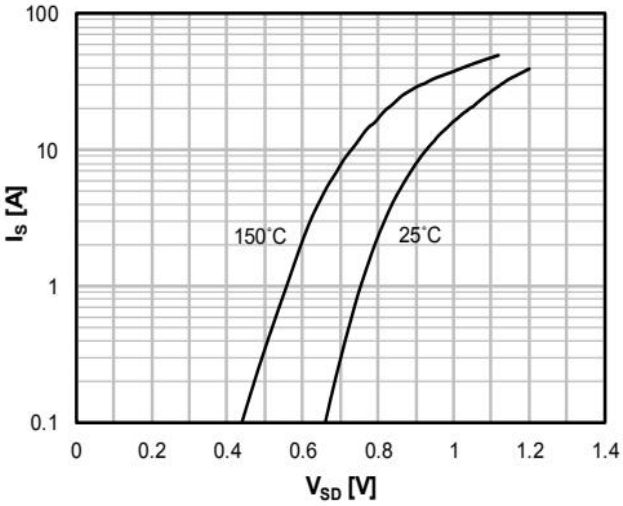


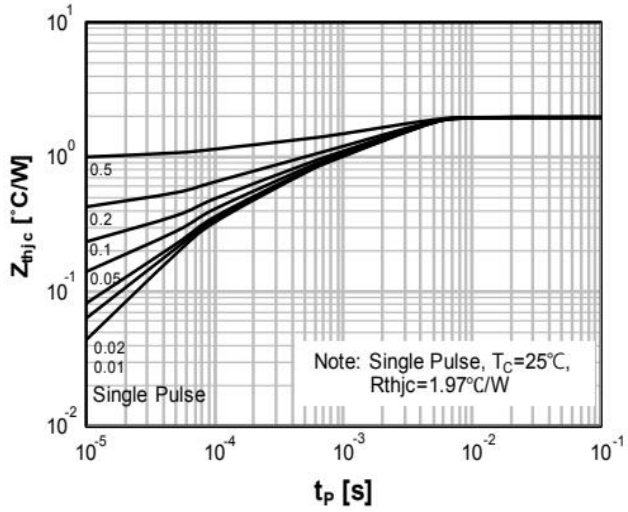
RATINGS AND CHARACTERISTIC CURVES





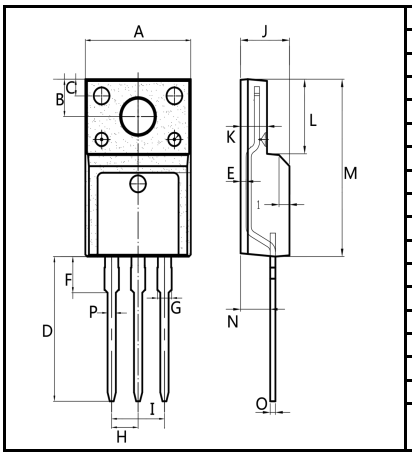






Package Outline Dimensions millimeters

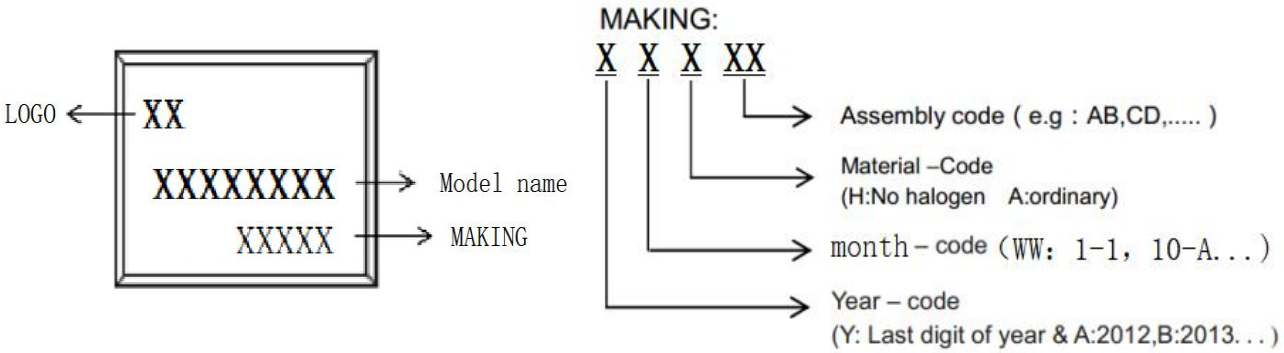
TO-220F



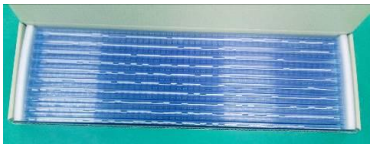
Dim.	Min.	Max.
A	9.95	10.35
B	2.95	3.25
C	1.25	1.45
D	12.8	13.2
E	0.4	0.6
F	2.8	3.5
G	1.3	1.45
H	Typ 2.54	
I	Typ 5.08	
J	4.5	5
K	2.45	2.65
L	6.5	6.8
M	15.4	16
N	2.75	3.05
O	0.45	0.55
P	0.7	0.9

All Dimensions in millimeter

Marking on the body



packing instruction

PKG	Minimal Package	Mini Box	Box
TO-220F			
	50pcs/pdpe	1000pcs/box	5000pcs/box

Notice

All product,product specifications and data are subject to change without notice to improve.The right to explain is owned by LINGXUN electronics company.

Confirm that operation temperature is within the specified range described in the product specification. Avoid applying poer exceeding normal rated

poer; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

LINGXUN electronics shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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Revision History

Rev	Change	Date
1.0	First version	2025-6-10