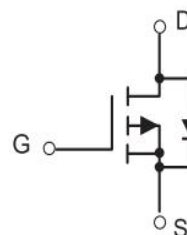


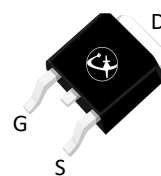
MAIN CHARACTERISTICS

I_D	-20 A
V_{DSS}	-100 V
$R_{DS(on)-typ}$ (@ $V_{GS}=-10V$) $T_C=25^{\circ}C$)	72m Ω



FEATURES

- Very Low On-resistance $R_{DS(ON)}$
- Low C_{rss}
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-252

APPLICATIONS

- PWM Application
- Load Switch
- Power Management

MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275 $^{\circ}C$ maximum, 10s per JESD 22-B106

Product specification classification

Part Number	Package	Mode Name	Pack
LT20P10AD	TO-252	LT20P10AD	Tape

Maximum Ratings at Tc=25°C unless otherwise specified

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continue Drain Current (Tc=25°C)	I_D	-20	A
Continue Drain Current (Tc=100°C)		-17	
Pulsed Drain Current (Note1)	I_{DM}	-80	A
Single Pulsed Avalanche Energy	E_{AS}	90	mJ
Power Dissipation	P_D	82	W
Operating Temperature Range	T_J	-55 to +150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.22	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	91	°C/W

Note1:Pulse test: 300 μ s pulse width, 2 % duty cycle

Electrical Characteristics at Tc=25°C unless otherwise specified

Parameter	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	BV_{DSS}	-100	-	-	V
Drain-Source Leakage Current	$V_{DS} = -100 \text{ V}, V_{GS} = 0 \text{ V}$	I_{DSS}	-	-	-1	μA
Gate Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	$V_{GS(th)}$	-1.5	-2	-2.5	V
Drain-Source On-State Resistance	$V_{GS} = -10 \text{ V}, I_D = -10 \text{ A}$	$R_{DS(on)}$	-	72	100	m Ω
	$V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$		-	78	110	m Ω
Input Capacitance	$V_{DS} = -50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$	C_{iss}	-	2859	-	pF
Output Capacitance		C_{oss}	-	93	-	pF
Reverse Transfer Capacitance		C_{rss}	-	68	-	pF
Turn-on Delay Time(Note2)	$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, R_G = 5 \Omega, I_D = -10 \text{ A}$	$t_{d(ON)}$	-	8	-	ns
Rise Time(Note2)		t_r	-	27	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	115	-	ns
Fall Time(Note2)		t_f	-	77	-	ns
Total Gate Charge(Note2)	$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -20 \text{ A}$	Q_G	-	53	-	nC
Gate to Source Charge(Note2)		Q_{GS}	-	12	-	nC
Gate to Drain Charge(Note2)		Q_{GD}	-	10	-	nC

Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified

Parameter	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current		I_S	-	-	-20	A
Maximun Body-Diode Pulsed Current(Note2)		I_{SM}	-	-	-80	A
Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = -20 \text{ A}, T_J = 25$	V_{SD}	-	0.81	1.1	V
Reverse Recovery Time(Note2)	$I_{SD} = -40 \text{ A}, V_{GS} = 0 \text{ V}, dI_F / dt = 100 \text{ A}/\mu\text{s}$	t_{rr}	-	36	-	ns
Reverse Recovery Charge(Note2)		Q_{rr}	-	40	-	nC

Note2:Pulse test: 300 μ s pulse width, 2 % duty cycle

RATINGS AND CHARACTERISTIC CURVES

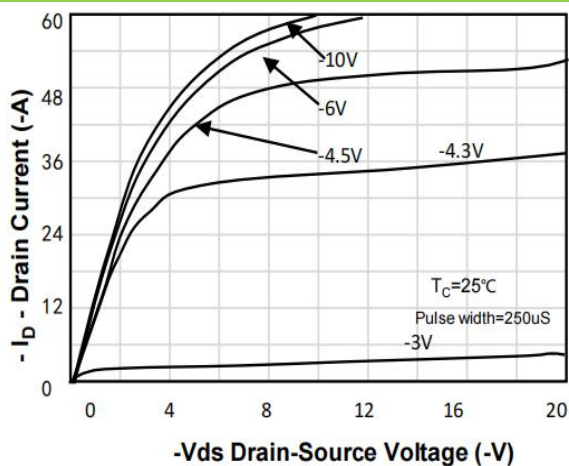


Figure 1. On-Region Characteristics

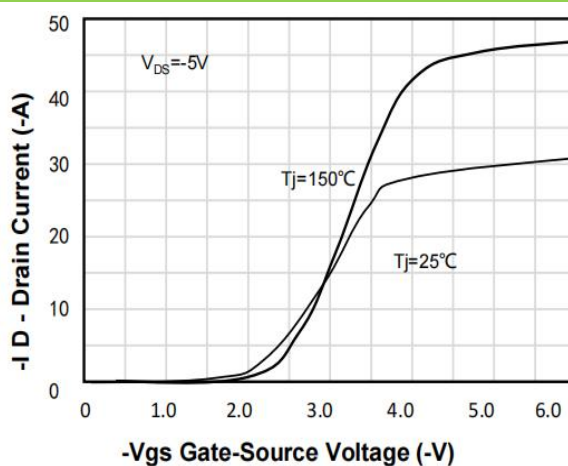


Figure 2. Transfer Characteristics

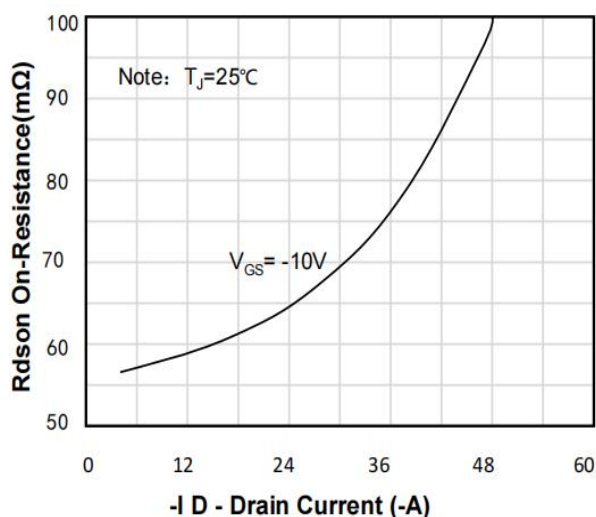


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

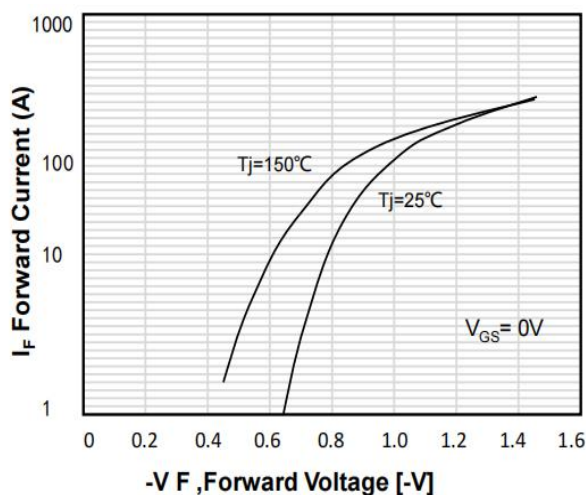


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

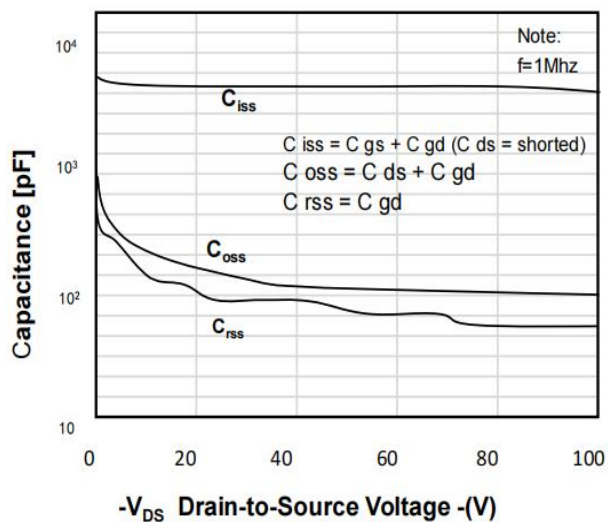


Figure 5. Capacitance Characteristics

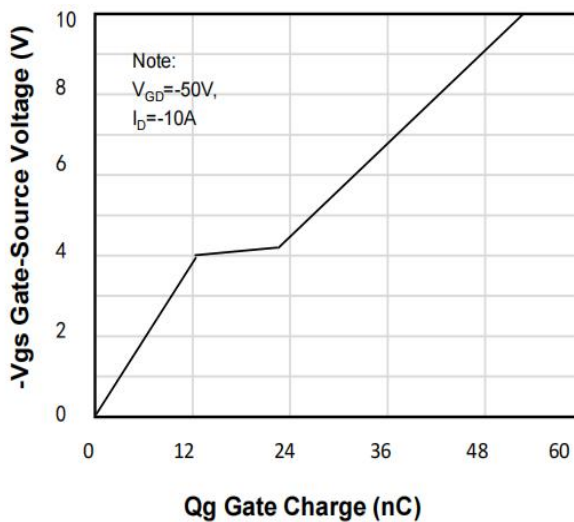


Figure 6. Gate Charge Characteristics

RATINGS AND CHARACTERISTIC CURVES

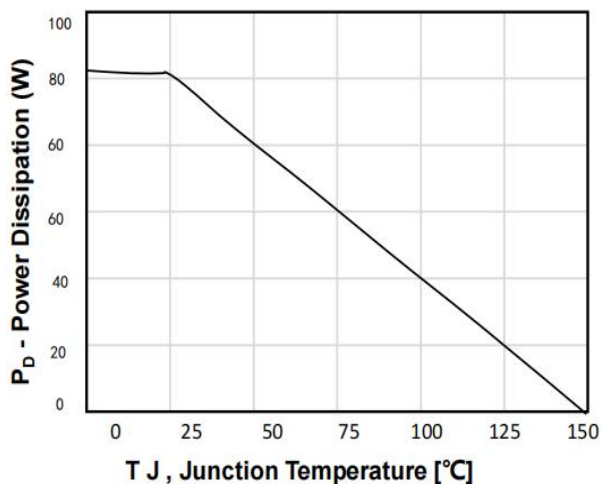


Figure 7. Power Dissipation Variation vs Temperature

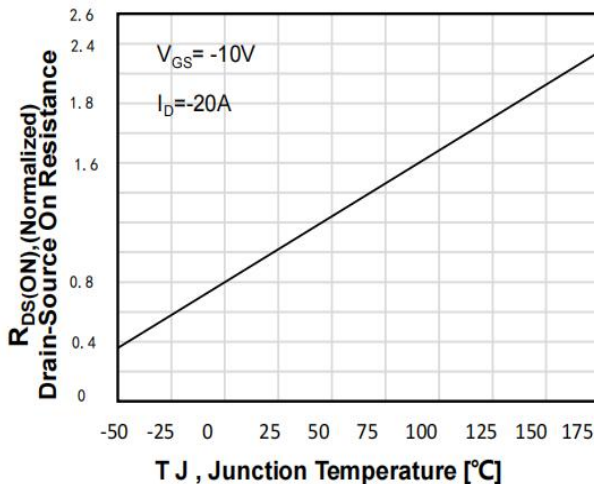


Figure 8. On-Resistance Variation vs Temperature

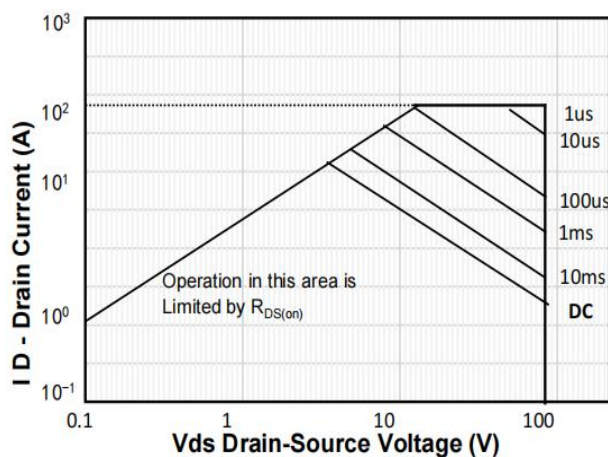


Figure 9. Maximum Safe Operating Area

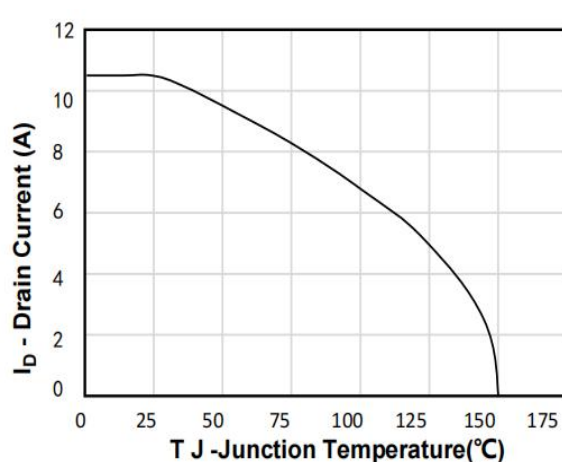


Figure 10. Maximum Continuous Drain Current vs Temperature

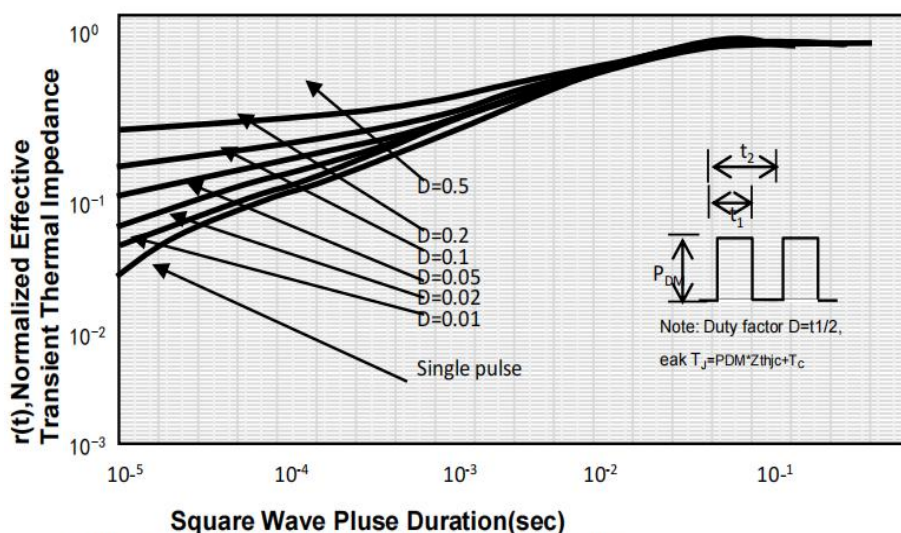
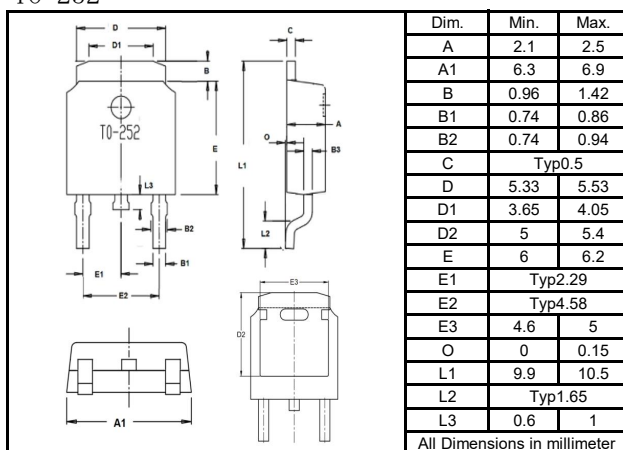


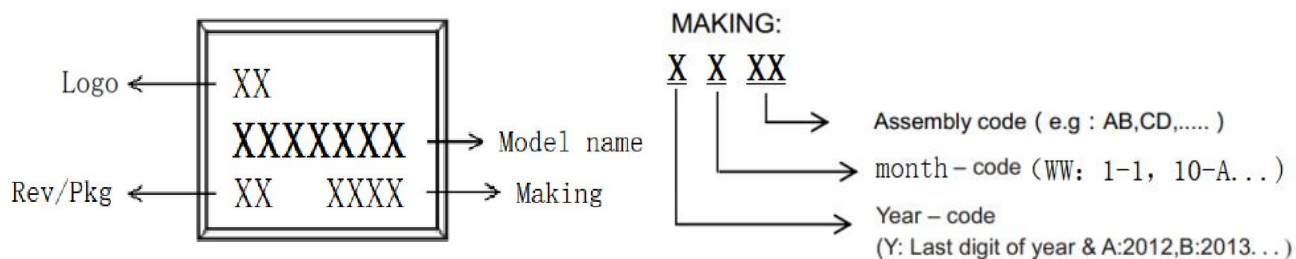
Figure 11. Transient Thermal Response Curve

Package Outline Dimensions millimeters

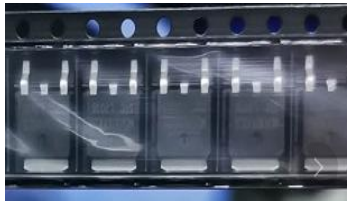
TO-252



Marking on the body



packing instruction

PKG	Minimal Package	Mini Box	Box
TO-252			
	2500pcs/disk	5000pcs/disk	25000pcs/box



LT20P10AD

P-Channel Enhancement Mode MOSFET

Notice

All product, product specifications and data are subject to change without notice to improve. The right to explain is owned by LINGXUN electronics company.

Confirm that operation temperature is within the specified range described in the product specification. Avoid applying poer exceeding normal rated

poer; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

LINGXUN electronics shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

<http://www.lxmicro.com>

Revision History

Rev	Changes	Date
1.0	First version	2025-9-5