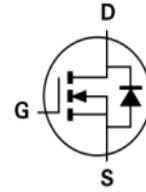


MAIN CHARACTERISTICS

I_D	40A
V_{DSS}	60V
$R_{DS(on)-typ}$ (@ $V_{GS}=10V$)	9m Ω

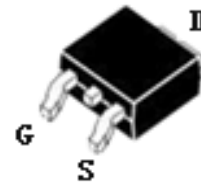


FEATURES

- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge

APPLICATIONS

- Load Switch
- PWM Application
- Power Management



TO-252

MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum, 10s per JESD 22-B106

Product specification classification

Part Number	Package	Mode Name	Pack
LT40N06AD	TO-252	LT40N06AD	Tape

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continue Drain Current	I_D	40	A
Pulsed Drain Current (Note1)	I_{DM}	160	A
Power Dissipation	P_D	113	W
Single Pulse Avalanche Energy (Note1)	E_{AS}	65	mJ
Operating Temperature Range	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.1	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	42	°C/W

Note1:Pulse test: 300 μ s pulse width, 2 % duty cycle

Electrical Characteristics at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	BV_{DSS}	60	-	-	V
Drain-Source Leakage Current	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	I_{DSS}	-	-	1	μA
Gate Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	$V_{GS(th)}$	1.2	-	2	V
Drain-Source On-State Resistance	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	$R_{DS(on)}$	-	9	12	m Ω
	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		-	11	13	m Ω
Input Capacitance	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$	C_{iss}	-	2635	-	pF
Output Capacitance		C_{oss}	-	124	-	pF
Reverse Transfer Capacitance		C_{rss}	-	98	-	pF
Turn-on Delay Time(Note2)	$V_{GS}=10\text{V}, V_{DD}=30 \text{ V}, R_G = 3 \Omega, I_D = 30 \text{ A}$	$t_{d(ON)}$	-	9	-	ns
Rise Time(Note2)		t_r	-	28	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	44	-	ns
Fall Time(Note2)		t_f	-	7	-	ns
Total Gate Charge(Note2)	$I_D = 30 \text{ A}, V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}$	Q_G	-	49	-	nC
Gate to Source Charge(Note2)		Q_{GS}	-	10	-	nC
Gate to Drain Charge(Note2)		Q_{GD}	-	9	-	nC

Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified

Characteristics	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current		I_S	-	-	40	A
Maximun Body-Diode Pulsed Current(Note2)		I_{SM}	-	-	160	A
Drain-Source Diode Forward Voltage	$I_{SD} = 30 \text{ A}$	V_{SD}	-	-	1.2	V
Body Diode Reverse Recovery Time	$I_F = 30\text{A}, di/dt = 100\text{A/us}$	t_{rr}	-	21	-	ns
Body Diode Reverse Recovery Charge		Q_{rr}	-	21	-	nC

Note2:Pulse test: 300 μ s pulse width, 2 % duty cycle

RATINGS AND CHARACTERISTIC CURVES

Figure 1: Power De-rating

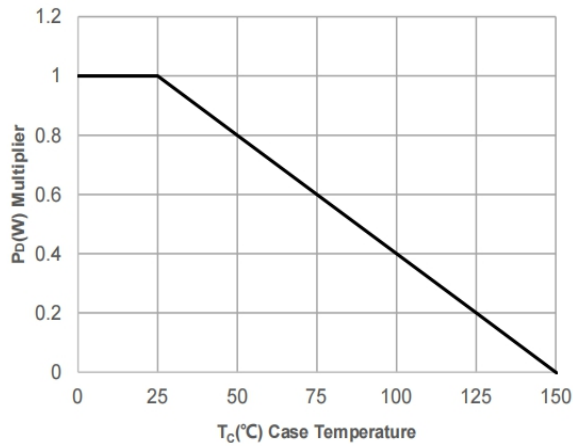


Figure 2: Current De-rating

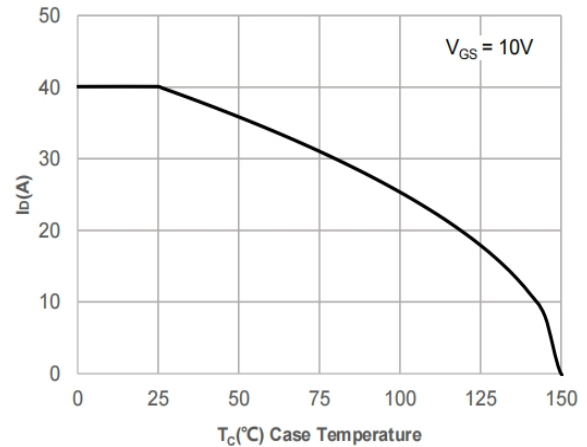


Figure 3: Normalized Maximum Transient Thermal Impedance

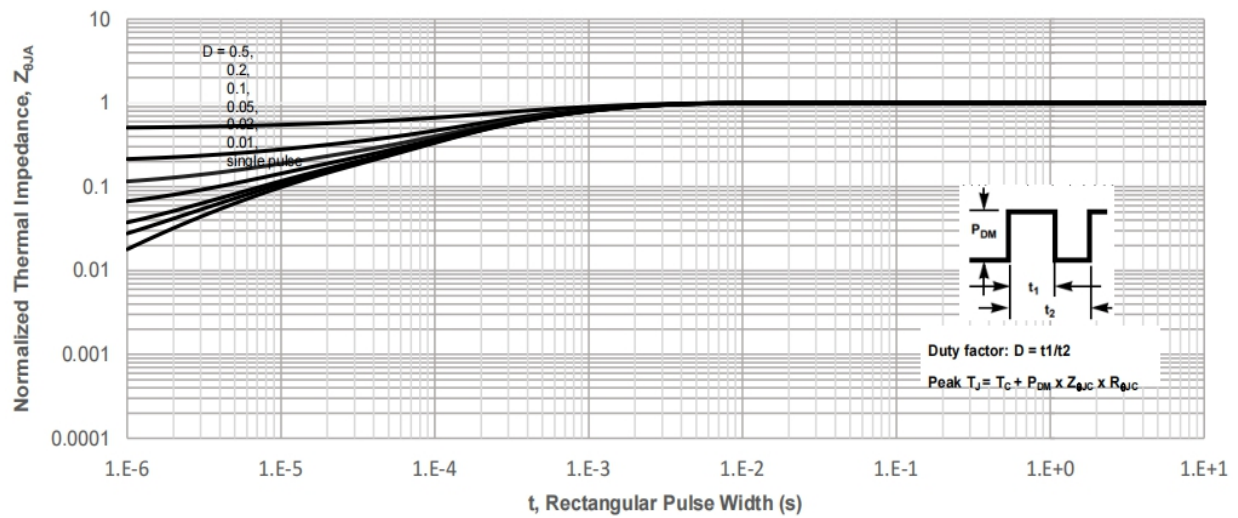
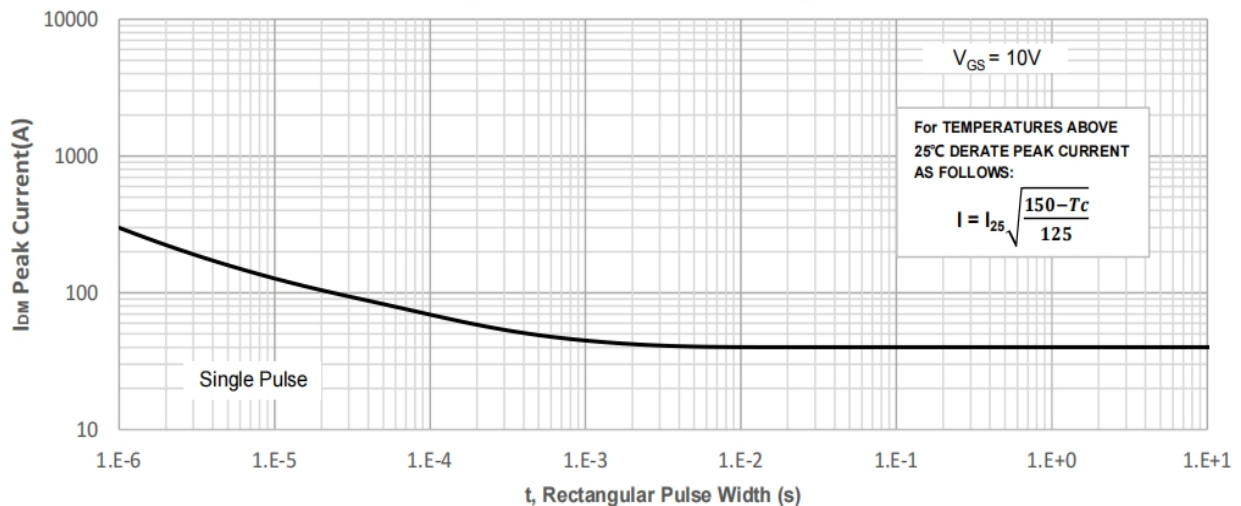


Figure 4: Peak Current Capacity



RATINGS AND CHARACTERISTIC CURVES

Figure 5: Output Characteristics

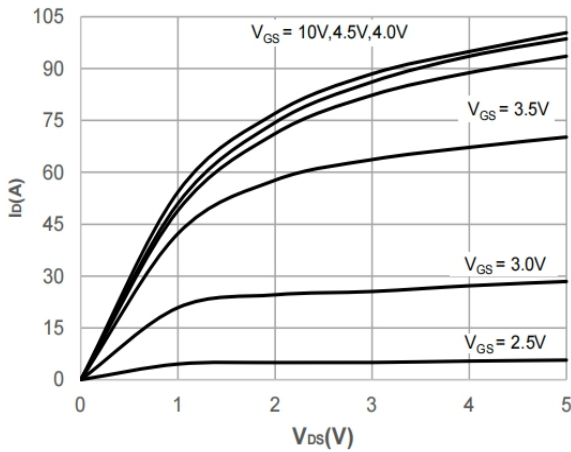


Figure 6: Typical Transfer Characteristics

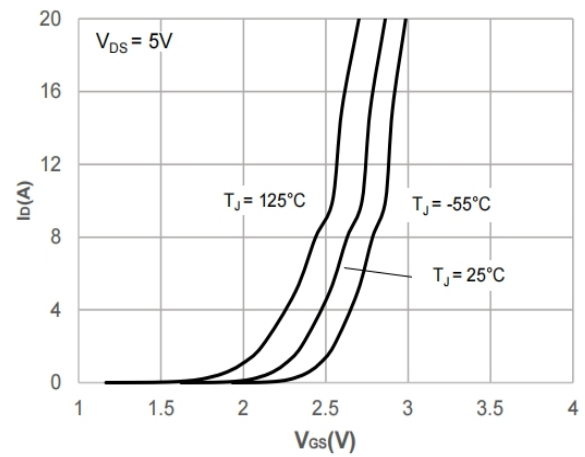


Figure 7: On-resistance vs. Drain Current

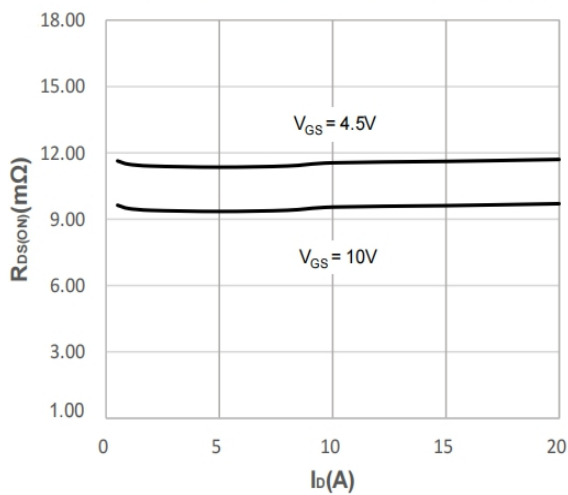


Figure 8: Body Diode Characteristics

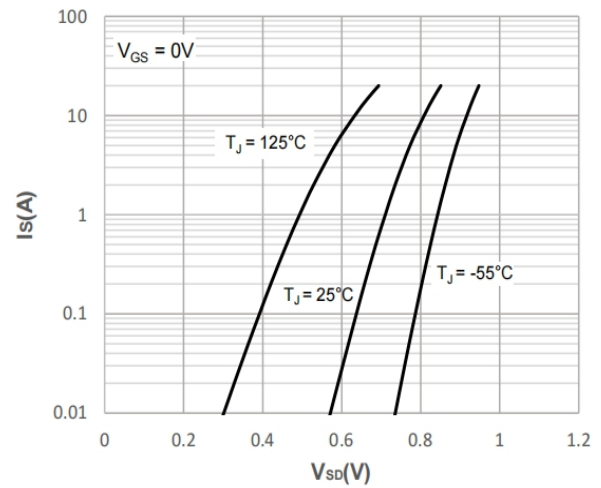


Figure 9: Gate Charge Characteristics

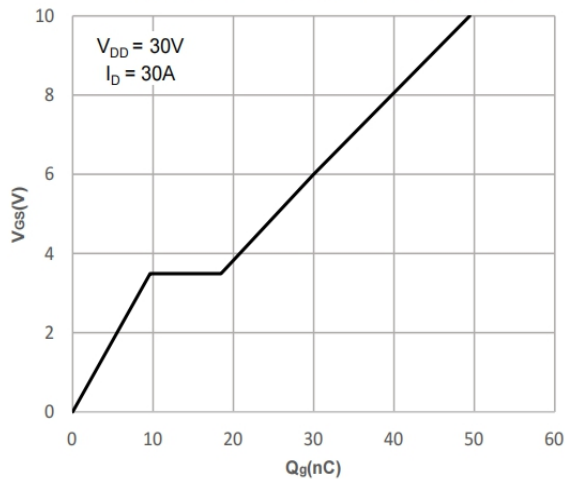
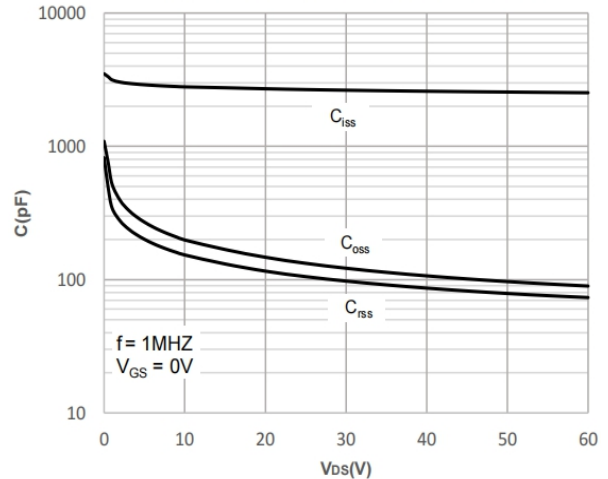


Figure 10: Capacitance Characteristics



RATINGS AND CHARACTERISTIC CURVES

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

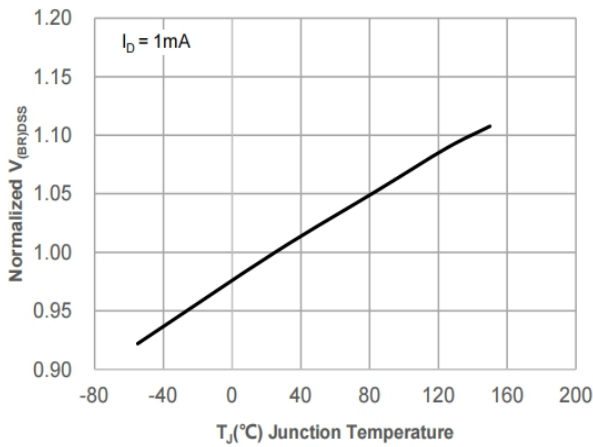


Figure 12: Normalized on Resistance vs. Junction Temperature

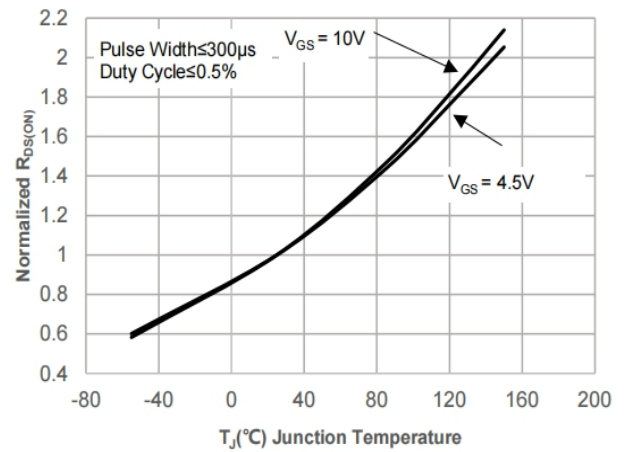


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

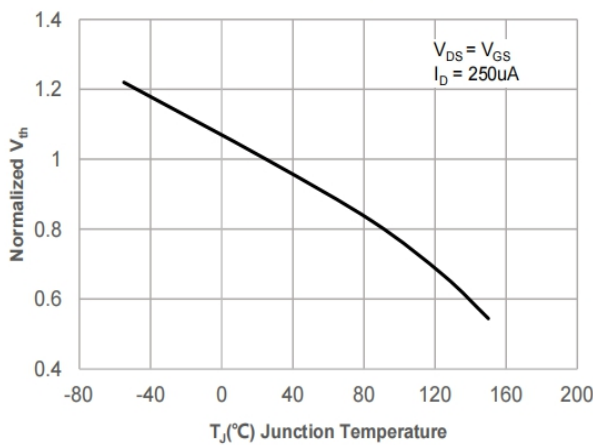


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

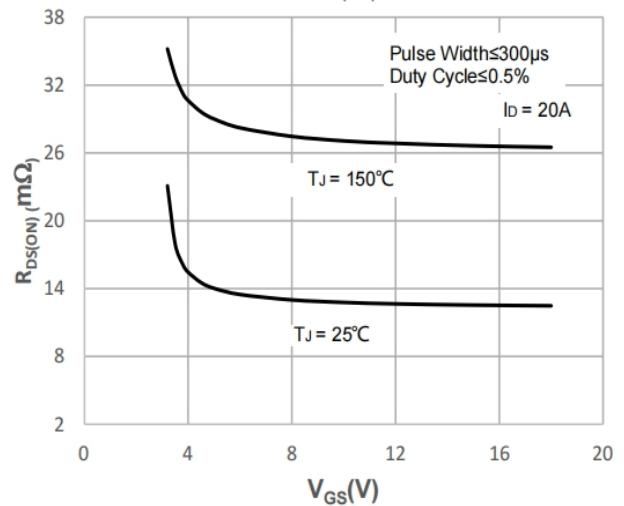
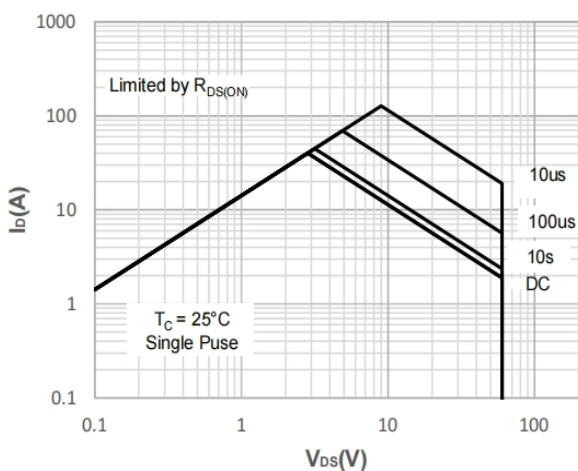
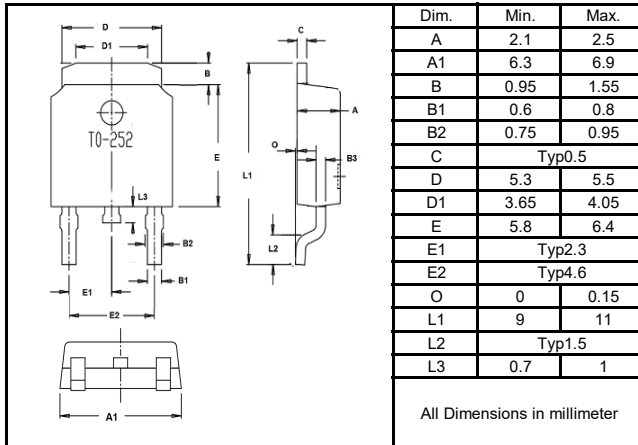


Figure 15: Maximum Safe Operating Area

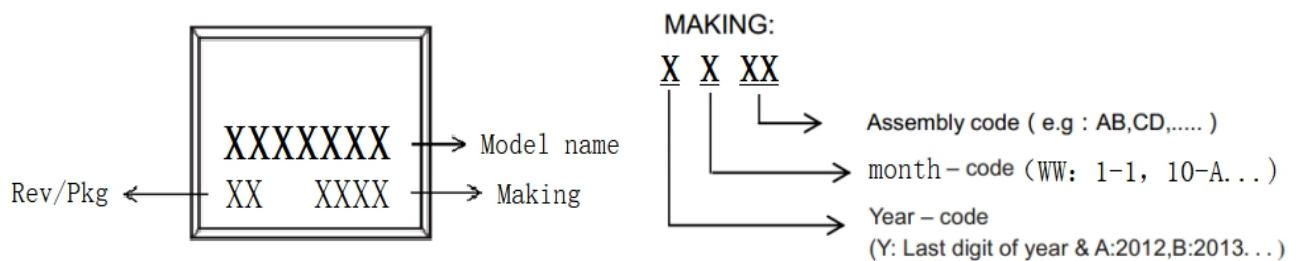


Package Outline Dimensions millimeters

TO-252



Marking on the body



packing instruction

PKG	最小包装	内盒	外箱
TO-252			
	2500pcs/盘	5000pcs/盒	25000pcs/箱

Notice

All product,product specifications and data are subject to change without notice to improve.The right to explain is owned by LINGXUN electronics

company.

Confirm that operation temperature is within the specified range described in the product specification. Avoid applying poer exceeding normal rated

poer; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

LINGXUN electronics shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.